

Docket No.: 071971-0203



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Akira KAWABE, et al.

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE

Application No.: 10/533,434

Filed: May 02, 2005

Customer No.: 53080  
Confirmation No.: 9488

Group Art Unit: 2627

Examiner: Christopher Ray Lamb

Title: PHASE ERROR DETECTING CIRCUIT AND SYNCHRONOUS CLOCK  
EXTRACTING CIRCUIT

**REQUEST FOR RECONSIDERATION**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following Remarks are submitted in response to the Office Action dated May 1,  
2007, pursuant to the provisions of 37 C.F.R. § 1.116.